

IN THE CLAIMS

Please cancel claims 18 and 19 without prejudice or disclaimer, and amend claims 20 and 22 as follows:

1 1. (Previously Presented) A plate for use in a plasma display panel, comprising:
2 a substrate;
3 a dielectric layer formed on a top surface of the substrate; and
4 partitions spaced a predetermined distance apart from each other and formed in a
5 snaking shape in a common direction to form a plurality of channels between adjacent
6 partitions, the channels including main discharge spaces and auxiliary discharge spaces
7 alternately arranged and connected to each other through the channels;
8 wherein the partitions comprise first partition portions forming the main discharge
9 spaces, second partition portions forming the auxiliary discharge spaces, and third partition
10 portions connecting the first and second partition portions; and
11 wherein widths of the first partition portions forming the main discharge spaces in
12 which red and green phosphors are coated are greater than widths of the first partition
13 portions forming the main discharge spaces in which a blue phosphor is coated.

Claim 2. (Canceled)

1 3. (Previously Presented) The plate of claim 1, the blue phosphor being coated on

2 the main discharge spaces at a thickness greater than a thickness of the red and green
3 phosphors.

1 4. (Previously Presented) A plate for a plasma display panel, comprising:
2 a substrate;
3 a dielectric layer formed on a top surface of the substrate;
4 first partitions formed in a striped pattern on a top surface of the dielectric layer and
5 spaced a predetermined distance apart from each other, the first partitions including non-
6 recessed portions and including recessed portions formed at opposite sides of the non-
7 recessed portions; and
8 second partitions spaced a predetermined distance apart from each other in a snaking
9 shape to form main discharge spaces in cooperation with the recessed portions, and to form
10 auxiliary discharge spaces in cooperation with lateral surfaces of the non-recessed portions.

1 5. (Previously Presented) The plate of claim 4, further comprising red and green
2 phosphors coated on respective main discharge spaces formed by adjacent pairs of the first
3 and second partitions, and a blue phosphor coated on main discharge spaces formed by
4 adjacent pairs of the second partitions.

1 6. (Previously Presented) The plate of claim 4, further comprising first and second
2 phosphors coated on respective first and second main discharge spaces formed by adjacent

3 pairs of the first and second partitions, and a third phosphor coated on third main discharge
4 spaces formed by adjacent pairs of the second partitions, each of the third main discharge
5 spaces including an open area accommodating the third phosphor, the open area of each of
6 the third main discharge spaces being greater than an open area of each of the first main
7 discharge spaces accommodating the first phosphor, and greater than an open area of each
8 of the second discharge spaces accommodating the second phosphor, the main discharge
9 spaces including the first, second, and third main discharge spaces.

Claim 7. (Canceled)

1 8. (Previously Presented) The plate of claim 6, further comprising sustaining and
2 common electrodes arranged at a boundary between the first and second main discharge
3 spaces and the third main discharge spaces.

1 9. (Previously Presented) The plate of claim 8, the sustaining and common electrodes
2 each including auxiliary electrodes positioned in the main discharge spaces.

1 10. (Previously Presented) The plate of claim 5, a width of the blue phosphor being
2 greater than a width of the red and green phosphors, respectively.

1 11. (Previously Presented) A plasma display panel, comprising:

2 a substrate;
3 data electrodes formed on a top surface of the substrate;
4 a first dielectric layer formed on the substrate to cover the data electrodes;
5 first partitions having a striped pattern, and spaced a predetermined distance apart
6 from each other and disposed on a top surface of the first dielectric layer, the first partitions
7 including recessed portions at opposite sides of non-recessed portions of the first partitions;
8 second partitions spaced a predetermined distance apart from each other in a snaking
9 shape to form main discharge spaces in cooperation with the recessed portions, and to form
10 auxiliary discharge spaces in cooperation with lateral surfaces of the non-recessed portions;
11 phosphors coated in the main discharge spaces;
12 a front plate sealed with the substrate;
13 common electrodes and sustaining electrodes arranged over the main discharge spaces
14 in a direction not parallel with a direction of the data electrodes on a bottom surface of the
15 front plate; and
16 a second dielectric layer formed on the bottom surface of the front plate to cover the
17 common and sustaining electrodes.

1 12. (Previously Presented) The plasma display panel of claim 11, the phosphors
2 comprising red and green phosphors coated on the respective main discharge spaces formed
3 by adjacent first and second partitions, and a blue phosphor coated on the respective main
4 discharge spaces formed by adjacent second partitions.

1 13. (Previously Presented) The plasma display panel of claim 12, a width of the blue
2 phosphor being greater than a width of the red and green phosphors.

1 14. (Previously Presented) The plasma display panel of claim 11, the common
2 electrodes and the sustaining electrodes being arranged above a boundary between first and
3 second main discharge spaces formed by adjacent first and second partitions and a third main
4 discharge space formed by adjacent second partitions.

1 15. (Original) The plasma display panel of claim 12, further comprising auxiliary
2 electrodes positioned in the main discharge spaces and extending toward one another from
3 opposing sides of the common and sustaining electrodes.

1 16. (Previously Presented) The plasma display panel of claim 11, the blue phosphor
2 being coated on the main discharge spaces at a thickness greater than a thickness of the red
3 and green phosphors.

1 17. (Previously Presented) The plate of claim 1, wherein red, green and blue
2 phosphors coated in the main discharge spaces are arranged in a triangular pattern with the
3 red and green phosphors being aligned with each other in a first direction and the blue
4 phosphors being located in a second direction relative to the red and green phosphors, the

5 second direction being substantially perpendicular to the first direction.

Claims 18 and 19. (Canceled)

1 20. (Currently Amended) A plate for use in a plasma display panel, comprising:
2 a substrate;
3 a dielectric layer formed on a top surface of the substrate; and
4 partitions spaced a predetermined distance apart from each other and formed in a
5 snaking shape in a common direction to form a plurality of channels between adjacent
6 partitions, the channels including main discharge spaces and auxiliary discharge spaces
7 alternately arranged and connected to each other through the channels;
8 wherein the partitions comprise first partition portions forming the main discharge
9 spaces, second partition portions forming the auxiliary discharge spaces, and third partition
10 portions connecting the first and second partition portions; and
11 wherein a width of the first partition portions is greater than both a width of the
12 second partition portions[[,]] and ~~is greater than~~ a width of the third partition portions.

1 21. (Previously Presented) A plasma display panel comprising the plate of claim 20,
2 and further comprising a common electrode and a sustaining electrode, each formed on the
3 plate;
4 wherein said common electrode and said sustaining electrode are arranged on the third

5 partition portions, whereby to increase an opening ratio of the main discharge spaces.

1 22. (Currently Amended) The plasma display panel of claim 21, further comprising
2 auxiliary electrodes positioned on the main discharge spaces and extending from opposing
3 sides of the common and sustaining electrodes, whereby to reduce a discharge [[state]]
4 starter voltage and extend a relative discharge area.